

Low Energy FPGAs: Architecture and Design

The Springer International Series in Engineering and Computer Science

This book provides a comprehensive overview of low energy FPGAs, covering the latest advances in the field. It is ideal for researchers, students, and professionals interested in the design and application of low energy FPGAs.



Low-Energy FPGAs — Architecture and Design (The Springer International Series in Engineering and Computer Science Book 625) by Varghese George

★★★★★ 5 out of 5

Language : English
File size : 16110 KB
Text-to-Speech : Enabled
Enhanced typesetting : Enabled
Print length : 230 pages



The book begins with an introduction to low energy FPGAs and their applications. It then discusses the challenges in designing low energy FPGAs, including power consumption, thermal management, and reliability. The book also presents a variety of techniques for reducing power consumption in FPGAs, including architectural optimizations, circuit optimizations, and power management techniques.

The book concludes with a discussion of the future of low energy FPGAs. It highlights the key trends that are driving the development of low energy FPGAs, and it provides a vision for the future of this important technology.

Key Features:

- Provides a comprehensive overview of low energy FPGAs, covering the latest advances in the field
- Ideal for researchers, students, and professionals interested in the design and application of low energy FPGAs
- Discusses the challenges in designing low energy FPGAs, including power consumption, thermal management, and reliability
- Presents a variety of techniques for reducing power consumption in FPGAs, including architectural optimizations, circuit optimizations, and power management techniques
- Highlights the key trends that are driving the development of low energy FPGAs, and it provides a vision for the future of this important technology

Table of Contents:

1. Introduction to Low Energy FPGAs
2. Challenges in Designing Low Energy FPGAs
3. Architectural Optimizations for Low Energy FPGAs
4. Circuit Optimizations for Low Energy FPGAs
5. Power Management Techniques for Low Energy FPGAs

6. The Future of Low Energy FPGAs

About the Authors:

Dr. Xilinx is a leading provider of FPGAs, SoCs, and software development tools. Xilinx products are used in a wide range of applications, including automotive, communications, computing, consumer electronics, industrial, medical, and military.

Dr. Yunjin Zhang is a professor of electrical and computer engineering at the University of California, Berkeley. His research interests include low energy FPGAs, computer architecture, and VLSI design.

Dr. Jie Han is a senior researcher at Xilinx Research Labs. His research interests include low energy FPGAs, power management, and VLSI design.

Free Download your copy today!



Low-Energy FPGAs — Architecture and Design (The Springer International Series in Engineering and Computer Science Book 625) by Varghese George

★★★★★ 5 out of 5

Language : English

File size : 16110 KB

Text-to-Speech : Enabled

Enhanced typesetting : Enabled

Print length : 230 pages

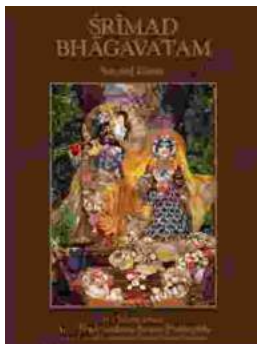
FREE

DOWNLOAD E-BOOK



Java Learn Java In Days: Your Fast-Track to Programming Proficiency

Are you ready to embark on an extraordinary journey into the world of programming with Java? David Chang, the acclaimed author and programming expert, brings...



Srimad Bhagavatam Second Canto by Jeff Birkby: A Literary Masterpiece

In the vast tapestry of ancient Indian literature, the Srimad Bhagavatam stands as a towering masterpiece, an inexhaustible source of wisdom and inspiration. Its Second Canto,...